

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,891,443 B2
DATED : April 26, 2005
INVENTOR(S) : Jean-Michel Ravon

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1,

Line 13, should read -- lation frequency can decrease with temperature. --

Column 2,

Line 65, should read -- M2 bypasses (shorts out) the third resistor R3, the voltage V_2 at --

Column 5,

Line 65, should read -- Circuitry as claimed in claim 2, wherein the threshold --

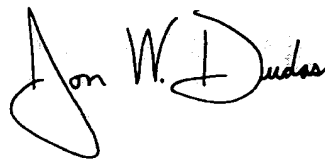
Column 6,

Line 45, should read -- resistive means comprises more than one diode connected transis --

Line 49, should read -- the capacitor and to trigger a change between the discharging --

Signed and Sealed this

Twelfth Day of July, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D" at the end.

JON W. DUDAS
Director of the United States Patent and Trademark Office